

Amendment and Response under 37 C.F.R. 1.116

Applicant: John Wade

Serial No.: 10/788,808

Filed: February 27, 2004

Docket No.: 200208190-1

Title: WIDE ARRAY FLUID EJECTION DEVICE**IN THE CLAIMS**

Please cancel claims 28 and 36 without prejudice.

Please amend claims 1, 18, and 27 as follows:

1. (Currently Amended) A fluid ejection device comprising:
 - a first set of N memory elements ~~each storing a fire enable value, the serially~~
receiving a series of fire enable values, each fire enable value including one of an enabling
value or a disabling value;
 - a second set of N memory elements ~~each storing a different one of N image data sub-~~
blocks of an image data block serially receiving N image data sub-blocks of an image data
block, each image data sub-block including one of an enabling value or a disabling value;
 - a third set of N memory elements ~~each storing and receiving a different one of~~
receiving in parallel the N image data sub-blocks from a different one of the second set of N
memory elements; and
 - N fluid ejecting elements each receiving the fire enable value from a corresponding
one of the first set of N memory elements and the image data sub-block from a corresponding
one of the third set of N memory elements, wherein one of the fluid ejecting elements is
enabled to eject a fluid when the fire enable value and the image data sub-block each are the
enabling value.
2. (Previously Presented) The fluid ejection device of claim 1, wherein the first set of N
memory elements and each of the N fluid ejecting elements are formed on a thin-film
structure formed on a substrate including a non-conductive material selected from a group
consisting of an oxide formed on a metal, a carbon composite material, a ceramic material,
and glass.
3. (Original) The fluid ejection device of claim 1, wherein the N fluid ejecting elements
are configured as a row that extends substantially for a width of a page of print media.
4. (Cancelled)

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5. (Previously Presented) The fluid ejection device of claim 1, wherein the image data block comprises a row of image data and each image data sub-block comprises a bit of image data.
6. (Cancelled)
7. (Previously Presented) The fluid ejection device of claim 1, wherein the first, second, and third sets of N memory elements each comprise a shift register having N memory elements.
8. (Previously Presented) The fluid ejection device of claim 1, wherein the third set of N memory elements is configured to receive the image data block from the second set of N memory elements in response to a load enable signal.
9. (Previously Presented) The fluid ejection device of claim 1, wherein after the third set of N memory elements receives the N image data sub-blocks from the second set of N memory elements, the second set of N memory elements is configured to serially receive and store N image data sub-blocks of a next image data block.
10. (Previously Presented) The fluid ejection device of claim 1, wherein each of the N fluid ejecting elements is configured to receive upon each cycle of a clock the image data sub-block from the corresponding one of the third set of N memory elements.
11. (Previously Presented) The fluid ejection device of claim 1, wherein the one of the fluid ejecting elements is not enabled to eject the fluid when one of the fire enable value or the image data sub-block is the disabling value.
12. (Previously Presented) The fluid ejection device of claim 1, wherein the N fluid ejecting elements are configured to print a block of image data in a print cycle, wherein the first set of N memory elements is configured to serially receive in the print cycle a series of

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fire enable values representative of a fire enable pulse, and wherein the first set of N memory elements receives a fire enable value upon each cycle of the clock, with a first fire enable value of the series being received upon a first clock cycle of the print cycle and a last fire enable value of the series being received upon a last clock cycle of the print cycle.

13. (Previously Presented) The fluid ejection device of claim 12, wherein a first X fire enable values of the series received during a first X clock cycles of the print cycle are enabling values and a remaining N fire enable values of the series received during a remaining N clock cycles of the print cycle are disabling values such that the enabling values propagate through the first set of N memory elements in a print cycle, wherein at an end of the print cycle each of the N memory elements of the first set of N memory elements is storing the disabling value.

14. (Original) The fluid ejection device of claim 13, wherein a product of X multiplied by a duration of the clock cycle substantially equals an enable pulse duration.

15. (Previously Presented) The fluid ejection device of claim 1, wherein each of the N fluid ejecting elements comprises:

a logic element configured to receive the fire enable value from the corresponding one of the first set of N memory elements and the image data sub-block from the corresponding one of the third set of N memory elements, and to provide a power switch control signal having a first state when the fire enable value and the image data sub-block each are the enabling value;

a heater resistor having a first terminal connectable to a power source and a second terminal; and

a switch coupled between the second terminal of the heater resistor and ground, the switch configured to receive the power switch control signal and connect the second terminal of the heater resistor to ground when the power switch control signal has the first state.

16. (Original) The fluid ejection device of claim 15, wherein the switch comprises:

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a field effect transistor having a gate coupled to the logic element, a drain coupled to the second terminal of the heater resistor, and a source coupled to ground.

17. (Previously Presented) The fluid ejection device of claim 15, wherein the logic element comprises:

an AND-gate having a first input coupled to the corresponding one of the first set of N memory elements, a second input coupled to the corresponding one of the third set of N memory elements, and an output providing the power switch control signal.

18. (Currently Amended) A fluid ejection device comprising:

a fire enable register including a series of N memory elements configured to serially receive and serially transfer a series of fire enable values through the series of N memory elements;

a data input register including a first set of N memory elements ~~each storing an image data bit~~ configured to serially receive N image data bits of a row of image data;

a data hold register including a second set of N memory elements ~~each coupled to and configured to receive the image data bit from a different one of~~ configured to receive in parallel the N image data bits from the first set of N memory elements; and

N fluid ejecting elements each coupled to and configured to receive one of the fire enable values from a different one of the series of N memory elements, and coupled to and ~~configured to receive the image data bit one of the image data bits~~ configured to receive the image data bit one of the image data bits from a different one of the second set of N memory elements, wherein each fluid ejecting element is enabled to eject a fluid when the one of the fire enable values and the ~~image data bit one of the image data bits~~ image data bit one of the image data bits each are an enabling value.

19. (Original) The fluid ejection device of claim 18, wherein the series of N memory elements and each of the N fluid ejecting elements are formed on a thin-film structure formed on a substrate including a non-conductive material selected from a group consisting of an oxide formed on a metal, a carbon composite material, a ceramic material, and glass.

20. (Cancelled)

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21. (Cancelled)

22. (Previously Presented) The fluid ejection device of claim 18, wherein the series of N memory elements and the first and second set of N memory elements each comprise a shift register having N memory elements.

23. (Previously Presented) The fluid ejection device of claim 18, wherein each of the N memory elements of the second set of N memory elements corresponds to a different one of the N memory elements of the first set of N memory elements, wherein the second set of N memory elements is configured to receive a present row of image data from the first set of N memory elements in response to a load enable signal, and wherein the first set of N memory elements is configured to serially receive a next row of image data after providing the present row of image data to the second set of N memory elements.

24. (Previously Presented) The fluid ejection device of claim 18, wherein each of the N fluid ejecting elements corresponds to a different one of the N memory elements of the second set of N memory elements and is configured to receive upon each cycle of a clock the image data bit from a corresponding one of the N memory elements, wherein the fluid ejecting element does not eject the fluid when either the one of the fire enable values or the image data bit is a disabling value.

25. (Previously Presented) The fluid ejection device of claim 18, wherein the N fluid ejecting elements are configured to print the row of image data in a print cycle.

26. (Previously Presented) The fluid ejection device of claim 25, wherein the series of N memory elements is configured to serially receive during the print cycle a fire enable pulse comprising a series of the fire enable values, wherein the series of N memory elements receives one fire enable value of the series upon each cycle of the clock.

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27. (Currently Amended) A method of enabling N fluid ejecting elements of a fluid ejection device, the method comprising:

serially receiving image data values in each of N memory elements of an image data input register, each memory element of the image data input register corresponding to a different one of N memory elements of an image data hold register;

parallel shifting the image data values from the N memory elements of the image data input register to the N memory elements of the image data hold register and holding the image data values in the N memory elements of the image data hold register~~holding an image data value in each of N memory elements of an image data hold register~~, each memory element of the image data hold register corresponding to a different one of the N fluid ejecting elements, each image data value being one of an enabling value or a disabling value;

~~storing a fire enable value~~ serially receiving fire enable values in each of N memory elements of a fire enable shift register, each memory element of the fire enable shift register corresponding to a different one of the N fluid ejecting elements, each fire enable value being one of an enabling value or a disabling value;

updating the fire enable value in each of the N memory elements of the fire enable shift register with a fire enable value from an adjacent memory element upon each cycle of a clock; and

upon each cycle of the clock, providing to each of the N fluid ejecting elements the fire enable value from the corresponding memory element of the fire enable shift register and the image data value from the corresponding memory element of the image data hold register, wherein a fluid ejecting element is enabled to eject a drop of fluid when the fire enable value and the image data value each are the enabling value.

28. (Cancelled)

29. (Cancelled)

30. (Original) The method of claim 27, further comprising:

receiving serially in a print cycle at the fire enable shift register a series of fire enable values representative of a fire enable pulse, wherein the fire enable shift register receives a

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fire enable value upon each clock cycle of the print cycle with a first enable value of the series being received upon a first clock cycle of the print cycle and a last fire enable value of the series being received upon a last clock cycle of the print cycle.

31. (Previously Presented) The method of claim 30, further comprising:

a first X fire enable values of the series being enabling values during a first X clock cycles of the print cycle and a remaining N fire enable values of the series having a disabling value during a remaining N clock cycles of the print cycle such that the first X fire enable values being enabling values propagate through the N memory elements of the fire enable shift register in a print cycle thereby sequentially enabling each of the N fluid ejecting elements to eject a drop of fluid for a duration substantially equal to a product of X multiplied by a duration of a clock cycle.

32-35. (Cancelled)

36. (Cancelled)

37-43. (Cancelled)